

100-443 U.S. PRO
09/733411
12/11/73

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. 1KW SCANNED <u>B(3)</u> O.A. <u>LA</u>	PATENT DATE
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APPLICATION NO. 09/733411	CONT/PRIOR D	CLASS 702 716	SUBCLASS 5	ART UNIT 2857 2825	EXAMINER DINH
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[illegible]

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Methodology to improve the performance of integrated circuits by exploiting systematic process non-uniformity

PTO-2040
12/89

ISSUING CLASSIFICATION									
ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ _____		ISSUE FEE	
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